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PWM CONTROL SIGNAL GENERATION METHOD AND APPARATUS

ABSTRACT

Disclosed is an apparatus for generating two constant width and symmetrical drive signals from two separate, complementary, pulse width modulated control signals while also generating two pulse width modulated drive signals corresponding to said pulse width modulated control signals. The constant width drive signals are generated through the use of a toggle or 10 latch set/reset circuit actuated by a given characteristic of each of the control signals.